

Application Number

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Group 21 Unit

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[illegible][illegible]

44	-	J. Chen et al., "Design and Layout of a High ESD Performance NPN Structure for Submicron BiCMOS Bipolar Circuits", IEEE Jnl. 1996, 0-7803-2753-5/96, pp. 227-232

J. MONDT

(09/18/05)

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.